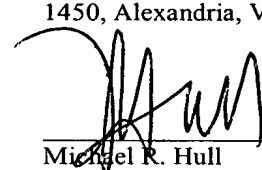




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**PATENT**  
**29925/39488**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Jae-Han Cha	)	I hereby certify that this paper and the
	)	documents referred to as enclosed
Serial No.: 10/630,280	)	therewith are being deposited with the
	)	United States Postal Service as first class
Filed: July 30, 2003	)	mail, postage prepaid, on <b>June 10, 2004</b> ,
	)	in an envelope addressed to
For: Method for Fabricating BiCMOS	)	Commissioner for Patents, P.O. Box
Transistor	)	1450, Alexandria, Virginia 22313-1450
	)	
Group Art Unit: 2825	)	
	)	
Examiner: Calvin Lee	)	
	)	Michael R. Hull
	)	Reg. No. 35,902
	)	Attorney for Applicants

**COMMENT ON STATEMENT OF REASONS FOR ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

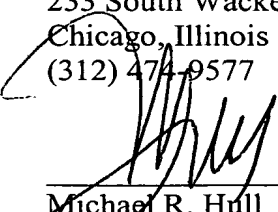
In addition to the reasons for allowance set forth in the allowance papers that were mailed in connection with the present application, it is respectfully submitted that the claims are allowable for the additional reasons that the invention defined by the language of the claims is neither anticipated by, nor would have been obvious when taken as a whole in view of, the art of record.

Respectfully submitted,

MARSHALL, GERSTEIN & BORUN LLP  
6300 Sears Tower  
233 South Wacker Drive  
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June 10, 2004

By:

  
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Michael R. Hull  
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